

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: F. ITO

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application
Serial No. 09/870,726, filed June 1, 2001

Filed: On Even Date Herewith

For: A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND A METHOD OF MANUFACTURING THE SAME

Art Group of Parent: 2813

Examiner of Parent: L. M. Schillinger

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 21, 2004

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO/SB/08A which list documents cited in parent application Serial No. 09/870,726, filed June 1, 2001, the entire disclosure of which is hereby incorporated by reference.


It is noted that a now abandoned related application U.S. Serial No. 10/329,441 was filed by the present inventor from the same parent application Serial No. 09/870,726 on December 27, 2002 and that all prior art cited in this abandoned related application is listed herein.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Docket No. 501.40147CX2).

Respectfully submitted,

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Complete if Known		
			Application Number	Not assigned yet	
			Filing Date	On even date herewith	
			First Named Inventor	F. ITO	
			Art Unit	Not assigned yet	
			Examiner Name	Not assigned yet	
Sheet	1	of	1	Attorney Docket Number	501.40147CX2

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
		5,754,467		05/1998	Ikeda et al.	
		5,780,910		07/1998	Hashimoto et al.	
		6,281,559		08/2001	Yu et al.	
		5,652,457		07/1997	Ikeda et al.	
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		6,479,392		11/2002	Yamazaki et al.	
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		6,262,456		07/2001	Yu et al.	
		US-2002/0001899		01/2001	ITO, F	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ –Number ⁴ –Kind Code ⁵ (if known)				
		JP 9-135029	05/1997			
		JP 9-92830	04/1997			
		JP 10-65151	03/1998			
		JP 7-321320	12/1995			
		JP 8-228000	09/1996			
		JP 11-274494	10/1999			
		JP 11-163317	08/1999			
		JP 00-12843	01/2000			
		JP 11-3990	01/1999			
		JP 2001284283 A	10/2001			

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		"Ultra-Low Leakage 0.16 μ m CMOS for Low-Standby Power Applications, Wu et al., 1999 IEDM (International Electron Device Meeting) Tech. Dig., pp. 671-674	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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